

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : Re. 35,496 Page 1 of 2
DATED : April 29, 1997
INVENTOR(S) : Yamamura et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Item 57, Abstract, Line 10, change "pan" to --part--;

Column 11, Line 11, after "surface" insert --,--;(2nd occur.)

Line 23, change "tint" to --first--;

Line 45, after "substantially" delete --.---

Line 54, after "encapsulating" delete --,--;

Line 65, change "pan" to --part--;

Column 12, Line 27, after "leads" insert --,--;

Line 32, before "plurality" insert --a--;

Line 57, after "inner" delete -- - --;

Line 61, change "chips" to --chip--;

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Line 61, after "chip" insert --,--;

Line 63, after "between" delete --,--.

Signed and Sealed this

Nineteenth Day of August, 1997

Attest:



BRUCE LEHMAN

Attesting Officer

Commissioner of Patents and Trademarks